

METHOD TO CREATE OPTIMIZED MACHINE CODE THROUGH  
COMBINED VERIFICATION AND TRANSLATION OF JAVA BYTECODE

Abstract of Disclosure

5           [0040.]The present invention is a new method and apparatus to perform  
combined compilation and verification of platform independent bytecode instruction  
listings into optimized machine code. More specifically, the present invention creates a  
new method and apparatus in which bytecode compilation instructions are combined with  
bytecode verification instructions, producing optimized machine code on the target  
10 system in fewer programming steps than traditionally known. The new method, by  
combining the steps required for traditional bytecode verification and compilation,  
increases speed and applicability of platform independent bytecode instructions.